

**PATENT NUMBER and  
ISSUE DATE**

**U.S. UTILITY Patent Application**

APPL NUM 10026245	FILING DATE 12/17/2001	CLASS 703	SUBCLASS 18	GAU 2123	EXAMINER BRUDA, JAT Feris
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\*\*APPLICANTS: Becker Scott;

**\*\*CONTINUING DATA VERIFIED:**

THIS APPLICATION IS A CON OF 09/442,877 11/18/1999  
WHICH IS A DIV OF 09/015,427 01/29/1998 PAT 6,016,390

**\*\* FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
ATTORNEY DOCKET NO ARTCP012C		
TITLE : Memories having reduced bitline voltage offsets		
U.S. DEPT. OF COMM./PAT & TM-PTO-438L(Rev. 12-94)		

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims 0	Print Claim for 0.0
Assistant Examiner		DRAWING	
		Sheets Drawn 0	Page Drawn 0
Primary Examiner		Print File	
<b>ISSUE FEE</b>		<b>Application Examiner</b>	
Amount Due	Date Paid		
TERMINAL			
<b>DISCLAIMER</b>		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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